

III Year I Semester  
17EC505

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### PULSE AND DIGITAL CIRCUITS

**UNIT I: LINEAR WAVESHAPING:** High pass, low pass RC circuits, their response for sinusoidal, step, pulse, square, ramp and exponential inputs. RC network as differentiator and integrator; Attenuators, its applications in CRO probe, RL and RLC circuits and their response for step input, Ringing circuit.

**UNIT II: NON-LINEAR WAVE SHAPING :** Diode clippers, Transistor clippers, Diode and Transistor clippers, clipping at two independent levels, Transfer characteristics of clippers, Emitter coupled clipper; Clamping operation, clamping circuits using diode with different inputs, Clamping circuit theorem, practical clamping circuits, effect of diode characteristics on clamping voltage, Transfer characteristics of clampers, comparators, applications of voltage comparators.

**UNIT III: SWITCHING CHARACTERISTICS OF DEVICES :** Diode as a switch, piecewise linear diode characteristics, Design and analysis of Transistor as a switch, Break down voltage consideration of transistor, saturation parameters of Transistor and their variation with temperature, Design of transistor switch, transistor-switching times. Introduction to Multivibrators.

**Bistable Multivibrator:** Analysis And Design of Fixed Bias, Self Bias Bistable Multi Vibrator, Collector Catching Diodes, Commutating Capacitors, Triggering of Binary Circuits, Emitter Coupled Bistable Multivibrator (Schmitt Trigger).

**UNIT IV: Monostable Multivibrator:** Analysis and Design of Collector Coupled Monostable Multi vibrator, Triggering of Monostable Multivibrator, Applications of Monostable Multivibrator.

**Astable Multivibrator:** Analysis and Design of Collector Coupled Astable Multivibrator, Application of Astable Multivibrator as a Voltage to Frequency Converter.

**UNIT V: VOLTAGE TIME BASE GENERATORS:** General features of a time base signal, Methods of generating time base waveform Exponential Sweep Circuits, Negative Resistance Switches, basic principles in Miller and Bootstrap time base generators, Transistor Miller time base generator, Transistor Bootstrap time base generator, Principles of synchronization,

**UNIT VI: LOGIC FAMILIES & SAMPLING GATES:** LOGIC FAMILIES: Diode Logic, Transistor Logic, Diode-Transistor Logic, Transistor-Transistor Logic, Emitter Coupled Logic, AOI Logic, Comparison of Logic Families. SAMPLING GATES: Basic Operating Principles of Sampling Gates, Diode Unidirectional Sampling Gate and Two-Diode Bi-Directional Sampling Gate, Four-Diode gates, Six-Diode Gates, Reduction of Pedestal in Sampling Gates, Applications of Sampling Gates.

**TEXT BOOKS:**

1. Pulse, Digital and Switching Waveforms - J. Millman and H. Taub, McGraw-Hill
2. Pulse and Digital Circuits – A. Anand Kumar, PHI, 2005

**REFERENCES :**

1. Pulse, Digital and Switching Waveforms - J. Millman and H. Taub, Mothiki S Prakash Rao McGraw-Hill, Second Edition, 2007.
2. Solid State Pulse circuits - David A. Bell, PHI, 4th Edn., 2002
3. Pulse & Digital Circuits by Venkata Rao,K,Ramasudha K, Manmadha Rao,G., Pearson,2010